Abstracts

A High-Speed Monolithic GaAs 10/11 Counter

R.E. Lundgren, D.E. Snyder and J.M. Lull. "A High-Speed Monolithic GaAs 10/11 Counter." 1981 MTT-S International Microwave Symposium Digest 81.1 (1981 [MWSYM]): 362-364.

Dual-gate GaAs MESFET logic has been used to develop a monolithic variable-modulus (/spl divide/10//spl divide/11) counter. The circuit incorporates a novel feedback design that will allow the counter to operate up to the maximum speed of its flip-flop components, which have demonstrated cutoff frequencies above 2.5 GHz. The full counter has operated to 1.6 GHz while mounted in a coplanar test fixture. Operation above 2 GHz is expected when a test fixture with reduced crosstalk is used.

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